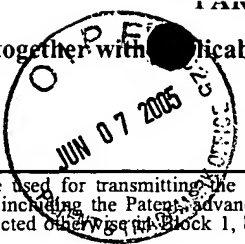


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38834 7590 05/11/2005  
**WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP**  
1250 CONNECTICUT AVENUE, NW  
SUITE 700  
WASHINGTON, DC 20036  
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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/650,072      | 08/28/2003  | Koji Takahashi       | 031038              | 5818             |

TITLE OF INVENTION: METHOD OF MANUFACTURING A MEMORY INTEGRATED CIRCUIT DEVICE

| APPLN. TYPE    | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE   |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO           | \$1400    | \$300           | \$1700           | 08/11/2005 |

| EXAMINER       | ART UNIT | CLASS-SUBCLASS |
|----------------|----------|----------------|
| GEYER, SCOTT B | 2812     | 438-258000     |

|   |  |
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)  
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(A) NAME OF ASSIGNEE **FUJITSU LIMITED** (B) RESIDENCE: (CITY and STATE OR COUNTRY) **KAWASAKI, JAPAN**

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Typed or printed name Stephen G. Adrian Registration No. 32,878

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